

APPLICANT(S): Goren et al
SERIAL NO.: 10/091,934
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REMARKS

Applicants elect to prosecute claims (43-45 and 47) of Group (2), drawn to an integrated circuit design library comprising transmission lines topologies for critical interconnects, wherein topologies comprise return paths and a set of parameterized, equivalent RLC ladder networks, classified in class 716, subclass 5.

Applicants thank Examiner Levin for his e-mail note of February 15 in response to our questions concerning claim 44. Thank you Examiner Levin for verifying that claim 44 should be listed in Group II. As such, we have responded in view of Examiner Levin's e-mail and have included claim 44 in Group II.

Applicant reserves the rights to prosecute the non-elected claims in a divisional or continuation application.

If the Examiner has any questions or comments as to this response, the undersigned may be contacted at the address and telephone number below.

Please charge any fees associated with this paper to deposit account No. (090468).

Respectfully submitted,

Suzanne Erez

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Dated: February 19, 2007